



TO : Greatwall

DATE : June. 21, 2011

SAMSUNG TFT-LCD**MODEL NO. : LTN101AT03**

NOTE : Extension code [1]
→ LTN101AT03-1
Surface type [**Anti-Glare**]

The information described in this SPEC is preliminary and can be changed without prior notice.

A handwritten signature in cursive script, appearing to read 'Samsong'.

APPROVED BY : _____

PREPARED BY : Application engineering group, TCS team

**Application engineering part, Mobile Division
Samsung Electronics Co., Ltd.**

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REVISION HISTORY

Approval

Date	Revision No.	Page	Summary
Jun. 21. 2011	A00	All	The specification of LTN101AT03-1 was issued first.

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Doc.No.

LTN101AT03-1

Rev.No

04-A00-G-110621

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GENERAL DESCRIPTION

DESCRIPTION

LTN101AT03 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as switching devices. This model is composed of a TFT LCD panel, a driver circuit and a backlight unit. The resolution of a 10.1" contains 1366 x 768 pixels and can display up to 262,144 colors. 6 O'clock direction is the optimum viewing angle.

FEATURES

- High contrast ratio
- HD (1366 x 768 pixels) resolution
- Low power consumption
- Fast Response
- LED Back Light with embedded LED Driver
- DE (Data enable) only mode
- 3.3V LVDS Interface
- Onboard EEDID chip
- Green product (RoHS compliant)

APPLICATIONS

- Notebook PC
- If the usage of this product is not for PC application, but for others, please contact SEC

GENERAL INFORMATION

Item	Specification	Unit	Note
Display area	222.73(H) x 125.22(V) (10.1" diagonal)	mm	
Driver element	a-Si TFT active matrix		
Display colors	262,144		
Number of pixel	1366 x 768	pixel	
Pixel arrangement	RGB vertical stripe		
Pixel pitch	0.16305(H) x 0.16305(V)	mm	TYP
Display Mode	Normally white		
Surface treatment	Haze 25% , Hard-Coating 3H		

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Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal (H)	234.5	235.0	235.5	mm	(1)
	Vertical (V)	142.5	143.0	143.5	mm	
	Depth (D)	-	-	5.2	mm	
Weight		-	-	210	g	

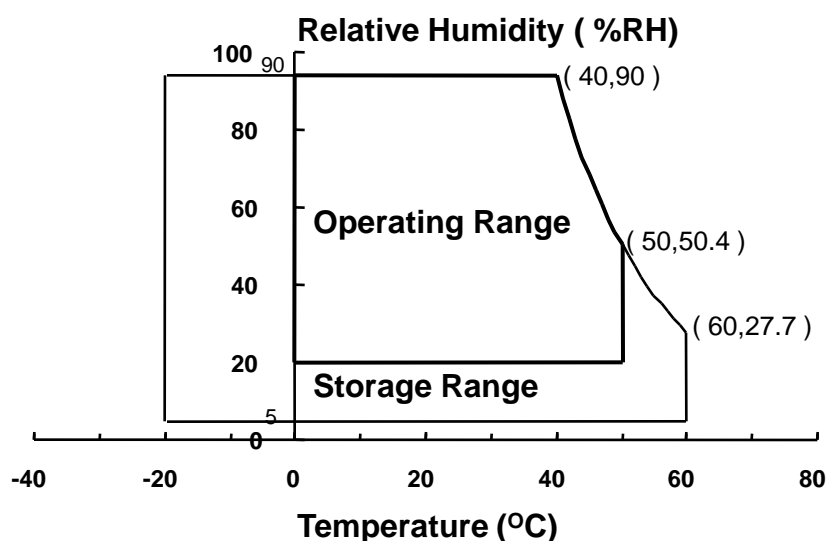
Note (1) Measurement condition of outline dimension
. Equipment : Bernier Calipers
. Push Force : 500g · f (minimum)

1. ABSOLUTE MAXIMUM RATINGS

1.1 ENVIRONMENTAL ABSOLUTE RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Storage temperate	TSTG	-20	60	°C	(1)
Operating temperate (Temperature of glass surface)	TOPR	0	50	°C	(1)
Shock (non-operating)	Snop	-	240	G	(2),(4)
Vibration (non-operating)	Vnop	-	2.41	G	(3),(4)

Note (1) Temperature and relative humidity range are shown in the figure below.
95 % RH Max. ($40\text{ }^{\circ}\text{C} \geq T_a$)
Maximum wet - bulb temperature at $39\text{ }^{\circ}\text{C}$ or less. ($T_a > 40\text{ }^{\circ}\text{C}$) No condensation



- (2) 2ms, half sine wave, one time for $\pm X$, $\pm Y$, $\pm Z$.
(3) 5 - 500 Hz, random vibration, 30min for X, Y, Z.
(4) At testing Vibration and Shock, the fixture in holding the Module to be tested have to be hard and rigid enough so that the Module would not be twisted or bent by the fixture.

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1.2 ELECTRICAL ABSOLUTE RATINGS

(1) TFT LCD MODULE

 $V_{DD}=3.3V$, $V_{SS}=GND=0V$

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	$V_{DD} - 0.3$	$V_{DD} + 0.3$	V	(1)
Logic Input Voltage	V_{IN}	$V_{DD} - 0.3$	$V_{DD} + 0.3$	V	(1)

Note (1) Within T_a (25 ± 2 °C)

2. OPTICAL CHARACTERISTICS

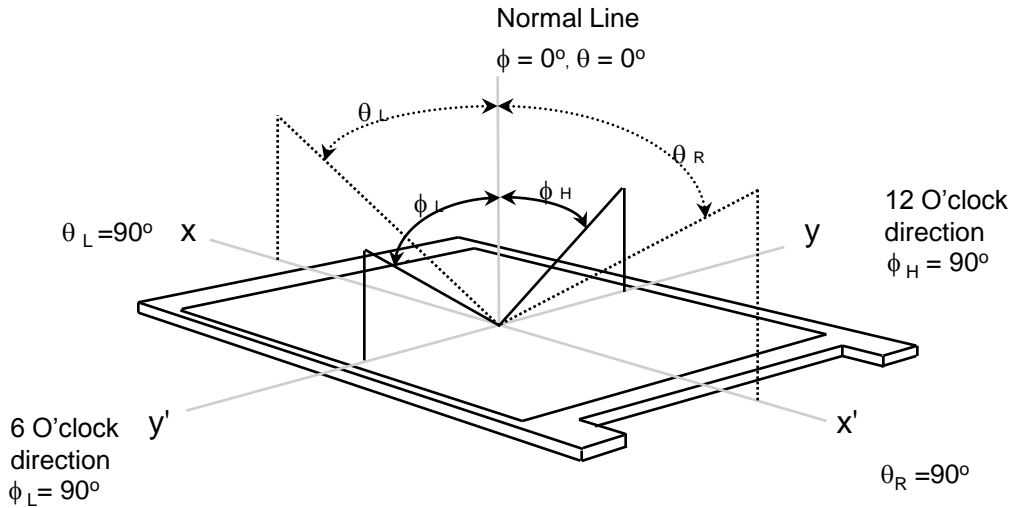
The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (5).
Measuring equipment : TOPCON SR-3

* Ta = 25 ± 2 °C, V_{DD}=3.3V, fv= 60Hz, fd_{CLK} = 66.19MHz, IL = 25.5 mA

Item		Symbol	Condition	Min.	Typ.	Max	Unit	Note
Contrast Ratio (5 Points)		CR	Normal Viewing Angle $\phi = 0$ $\theta = 0$	300	-	-	-	(1), (2), (5)
Response Time at Ta (Rising + Falling)		T _{RT}		-	16	25	msec	(1), (3)
Average Luminance of White (5 Points)		Y _{L,AVE}		170	200	-	cd/m ²	IF=20.0mA (1), (4)
Color Chromaticity (CIE)	Red	R _X		0.523	0.553	0.583	-	(1), (5) SR-3
		R _Y		0.317	0.347	0.377		
	Green	G _X		0.322	0.352	0.382		
		G _Y		0.556	0.586	0.616		
	Blue	B _X		0.135	0.165	0.195		
		B _Y		0.080	0.110	0.140		
	White	W _X		0.283	0.313	0.343		
		W _Y	0.299	0.329	0.359			
Color Gamut			42	45	-	%		
Viewing Angle	Hor.	θ_L	CR ≥ 10 At center	40	-	-	Degrees	(1), (5) SR-3
		θ_H		40	-	-		
	Ver.	ϕ_H		15	-	-		
		ϕ_L		30	-	-		
	Hor.	θ_L	CR ≥ 100	25	-	-	Degrees	
		θ_R		25	-	-		
	Ver.	ϕ_H		5	-	-		
		ϕ_L		15	-	-		
13 Points White Variation		δ_L		-	-	1.54	-	(6)

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Note 1) Definition of Viewing Angle : Viewing angle range($10 \leq C/R$)

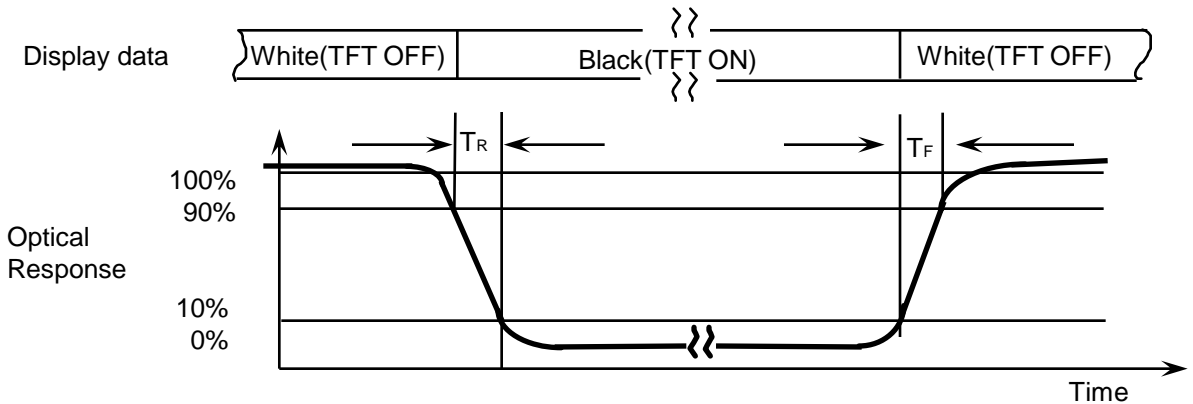


Note 2) Definition of Contrast Ratio (CR) : Ratio of gray max (Gmax) ,gray min (Gmin) at 5 points(4, 5, 7, 9, 10)

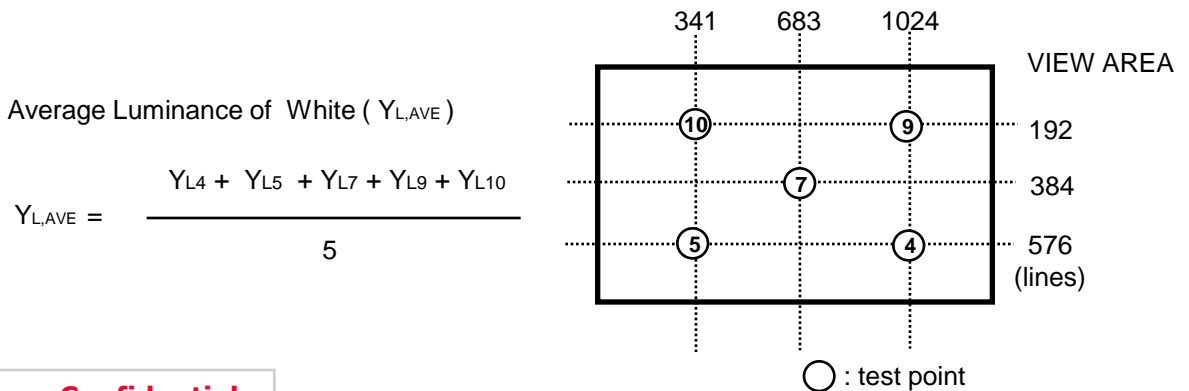
$$CR = \frac{CR(4) + CR(5) + CR(7) + CR(9) + CR(10)}{5}$$

Points : (4) , (5) , (7) , (9) , (10) at the figure of Note (6).

Note 3) Definition of Response time :



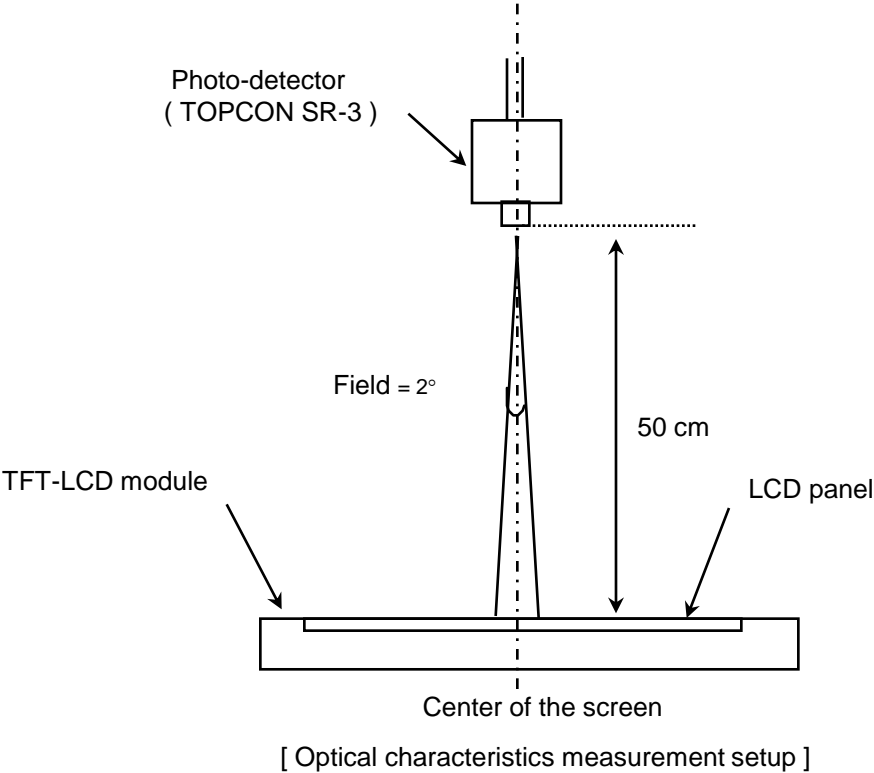
Note 4) Definition of Average Luminance of White : measure the luminance of white at 5 points.



Average Luminance of White ($Y_{L,AVE}$)

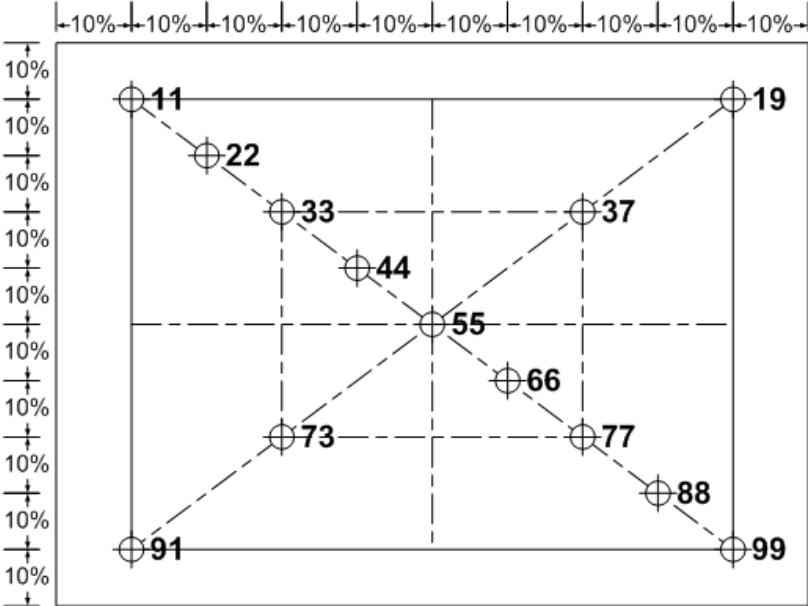
$$Y_{L,AVE} = \frac{Y_{L4} + Y_{L5} + Y_{L7} + Y_{L9} + Y_{L10}}{5}$$

Note 5) After stabilizing and leaving the panel alone at a given temperature for 30 min , the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. 30 min after lighting the backlight. This should be measured in the center of screen.
 LED current : 22.0mA
 Environment condition : Ta = 25 ± 2 °C



Note 6) Definition of 13 points white variation (δL), CR variation(C_{VER}) [① ~ ⑬]

$$\delta L = \frac{\text{Maximum luminance of 13 points}}{\text{Minimum luminance of 13 points}}$$



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3. ELECTRICAL CHARACTERISTICS

3.1 TFT LCD MODULE

Ta= 25 ± 2°C

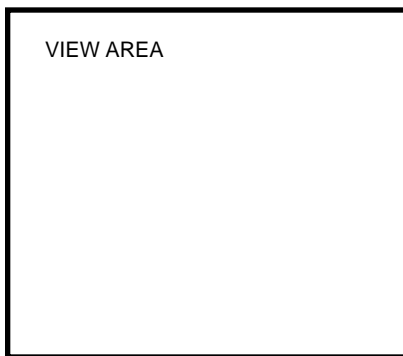
Item	Symbol	Min.	Typ.	Max.	Unit	Note	
Voltage of Power Supply	V _{DD}	3.0	3.3	3.6	V		
Differential Input Voltage for LVDS Receiver Threshold	High	V _{IH}	-	-	+100	mV	V _{CM} = +1.2V
	Low	V _{IL}	-100	-	-	mV	
Vsync Frequency	f _v	-	60	-	Hz		
Hsync Frequency	f _H	-	46.68	-	KHz		
Main Frequency	f _{DCLK}	-	66.19	-	MHz		
Rush Current	I _{RUSH}	-	-	1.5	A	(4)	
Current of Power Supply	White	I _{DD}	-	250	-	mA	(2),(3)*a
	Mosaic		-	280	300	mA	(2),(3)*b
	V. stripe		-	320	-	mA	(2),(3)*c

Note (1) Display data pins and timing signal pins should be connected.(GND = 0V)

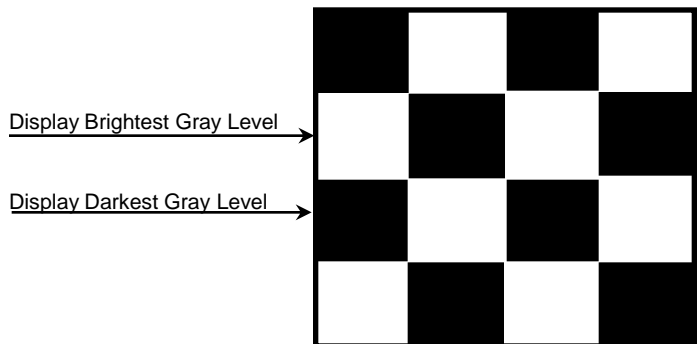
(2) f_v = 60Hz, f_{DCLK} = 66.19MHz, V_{DD} = 3.3V , DC Current.

(3) Power dissipation pattern

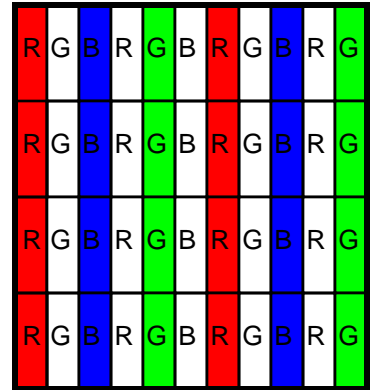
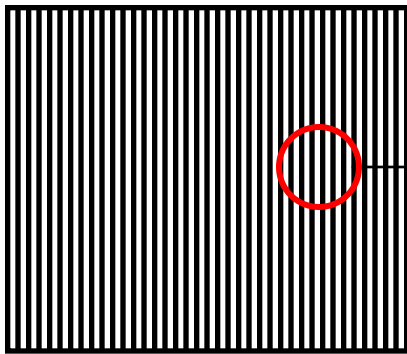
*a) White Pattern



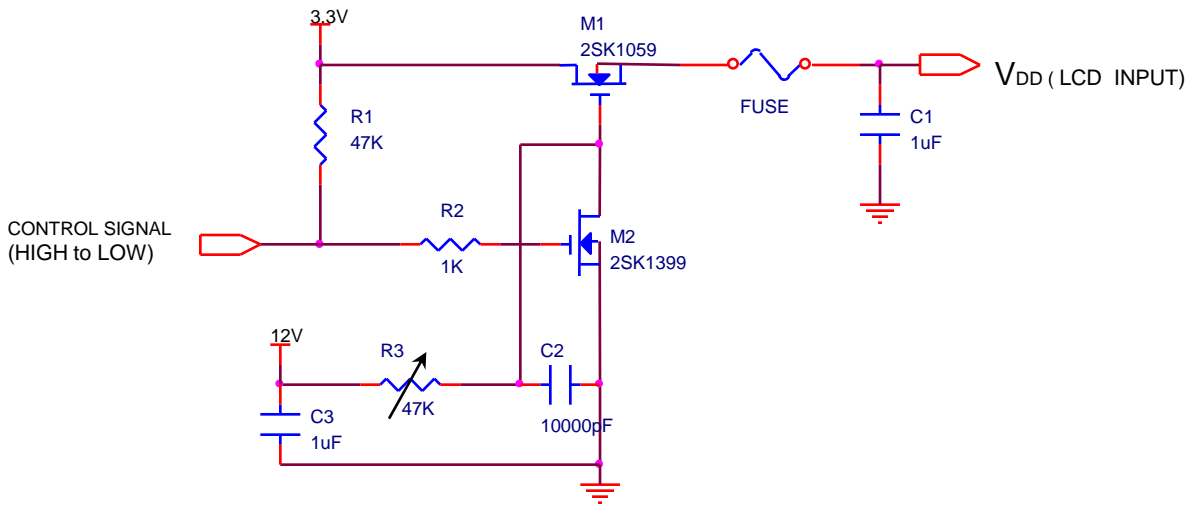
*b) Mosaic Pattern



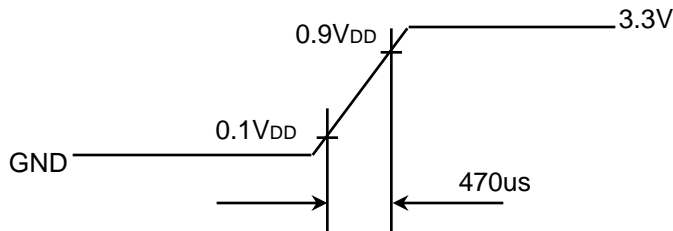
*c) 1dot Vertical stripe pattern



4) Rush current measurement condition



V_{DD} rising time is 470us



3.2 BACK-LIGHT UNIT

Ta= 25 ± 2 °C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
LED Forward Current	IF	-	25.5	-	mA	
LED Forward Voltage	VF	-	3.2	-	V	
LED Array Voltage	VP	-	19.2	-	V	VF X 6 LEDs
Power Consumption	P	-	1.96	2.5	W	IF X VF X 24 LEDs
Operating Life Time	Hr	15000	-	-	Hour	(1)

Note (1) Life time (Hr) of LEDs can be defined as the time in which it continues to operate under the condition Ta= 25 ± 2 °C and IF = 22.0 mArms until one of the following event occurs.

1. When the brightness becomes 50% or lower than the original.

3.3 LED Driver

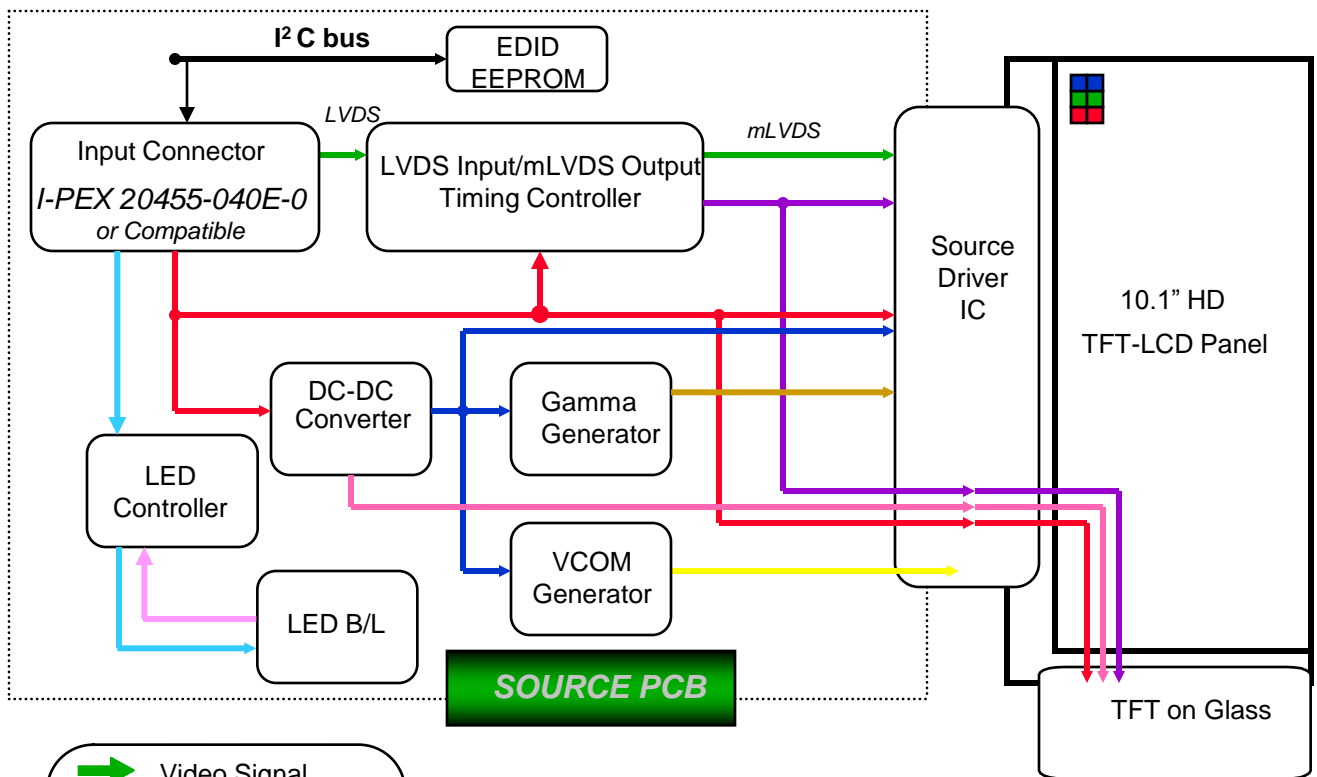
Ta= 25 ± 2 °C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Input Voltage	V _{in}	6	12	20	V	
Enable Control Level	V	0	-	5	V	ON Level : 2V~3.3V OFF Level : 0V ~ 0.5V
PWM Control Level	V _{PWM}	0		5	V	High Level : 1.5V~5.0V Low Level : 0V ~ 0.1V
PWM Control Duty Ratio	%	10	-	100	%	
Minimum on Time		-	100	-	ns	
Operating Frequency	Hz	0.2	-	1	KHz	

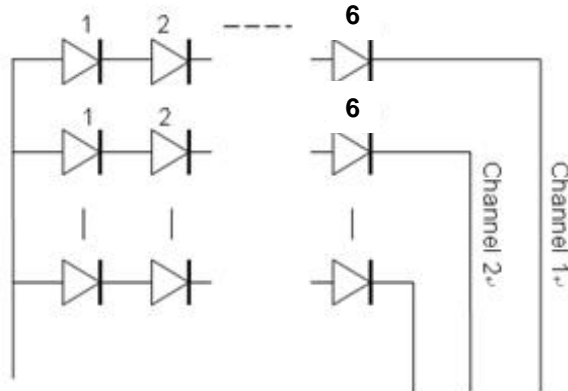
Note - Test Equipment : Fluke 45

4. BLOCK DIAGRAM

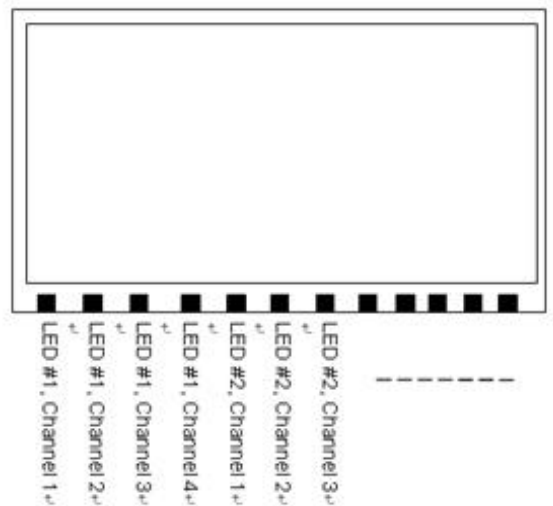
4.1 TFT LCD Module



4.2 LED connection and placement



LED Wiring



LED Placement

5. INPUT TERMINAL PIN ASSIGNMENT

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5.1. Input Signal & Power (LVDS, Connector : I-PEX 20455-040E-## or equivalent)

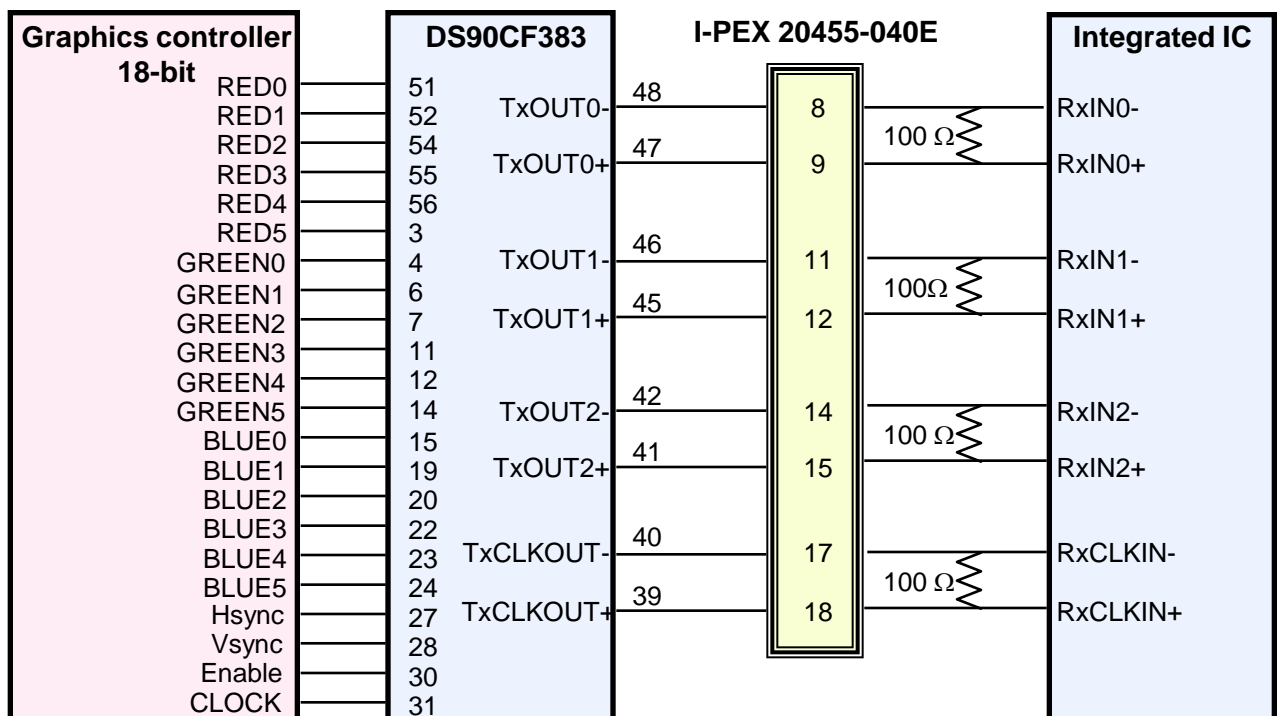
No.	Symbol	Function	Polarity	Remarks
1	Diag loop	Diag pin for Dell testing.		
2	AVDD	Power Supply 3.3V (typical)		
3	AVDD	Power Supply 3.3V (typical)		
4	DVDD	DDC 3.3V power		
5	Test	Panel self test		
6	SCL	DDC Clock		
7	SDA	DDC Data		
8	RIN0-	LVDS differential data input (R0-R5, G0)	Negative	
9	RIN0+	LVDS differential data input (R0-R5, G0)	Positive	
10	GND	Ground		
11	RIN1-	LVDS differential data input (G1-G5, B0-B1)	Negative	
12	RIN1+	LVDS differential data input (G1-G5, B0-B1)	Positive	
13	GND	Ground		
14	RIN2-	LVDS differential data input (B2-B5, HS, VS, DE)	Negative	
15	RIN2+	LVDS differential data input (B2-B5, HS, VS, DE)	Positive	
16	GND	Ground		
17	CLK-	LVDS differential clock input	Negative	
18	CLK+	LVDS differential clock input	Positive	
19	GND	Ground		
20	NC	No Connection		
21	NC	No Connection		
22	GND	Ground		
23	NC	No Connection		
24	NC	No Connection		
25	GND	Ground		
26	NC	No Connection		
27	NC	No Connection		
28	GND	Ground		
29	NC	No Connection		
30	NC	No Connection		

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No.	Symbol	Function	Polarity	Remarks
31	VBL-	LED Ground		
32	VBL-	LED Ground		
33	VBL-	LED Ground		
34	Diag loop	Diag pin for Dell testing.		
35	BLIM	PWM for luminance control(200~1KHz, 3.3V, 10~100%, 0V=off) 5V tolerant		
36	BL_Enable	BL On/Off(On:2.0~3.3V, Off:0~0.5V) / NC(100K pull-up) / 5V tolerant		
37	NC	No Connection		
38	VBL+	LED Power Supply 6V ~ 20V		
39	VBL+	LED Power Supply 6V ~ 20V		
40	VBL+	LED Power Supply 6V ~ 20V		

5.2 LVDS Interface : Transmitter DS90CF363 or Compatible

Pin No.	Name	RGB Signal	Pin No.	Name	RGB Signal
51	TxIN0	R0	14	TxIN14	G5
52	TxIN1	R1	15	TxIN15	B0
54	TxIN2	R2	19	TxIN18	B1
55	TxIN3	R3	20	TxIN19	B2
56	TxIN4	R4	22	TxIN20	B3
3	TxIN6	R5	23	TxIN21	B4
4	TxIN7	G0	24	TxIN22	B5
6	TxIN8	G1	27	TxIN24	Hsync
7	TxIN9	G2	28	TxIN25	Vsync
11	TxIN12	G3	30	TxIN26	DE
12	TxIN13	G4	31	TxCLKIN	Clock

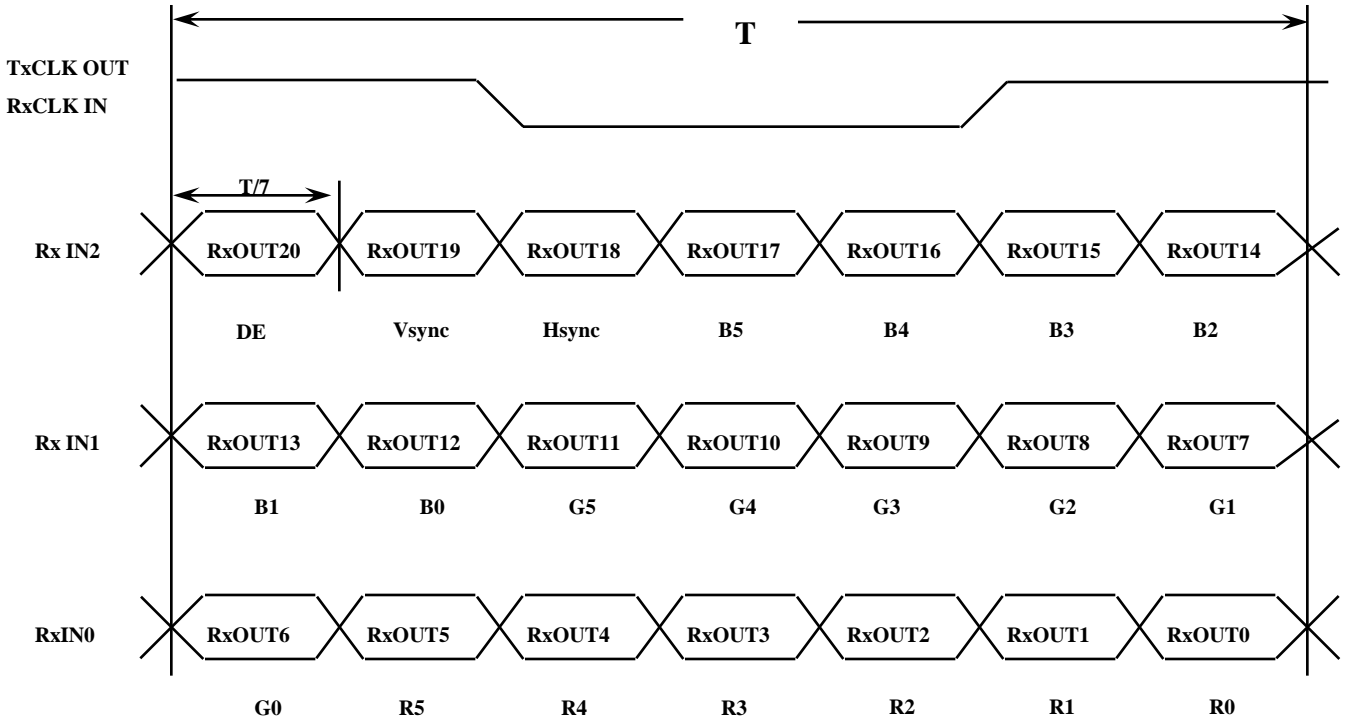
LVDS INTERFACE

Note : The LCD Module uses a 100ohm resistor between positive and negative lines of each receiver input.

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5.3 Timing Diagrams of LVDS For Transmission

LVDS Receiver : Integrated T-CON



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5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color

Color	Display	Data Signal																	Gray Scale Level	
		Red					Green					Blue								
		R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	45		B5
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	-
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	-
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	-
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	-
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1	-
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	-
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
Gray Scale Of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	Dark	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	↑	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R61
	Light	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R62
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R63
Gray Scale Of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	Dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	G1
	↑	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	G2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	G61
	Light	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	G62
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	G63
Gray Scale Of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	Dark	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	B1
	↑	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	B2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	B61
	Light	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	B62
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	B63

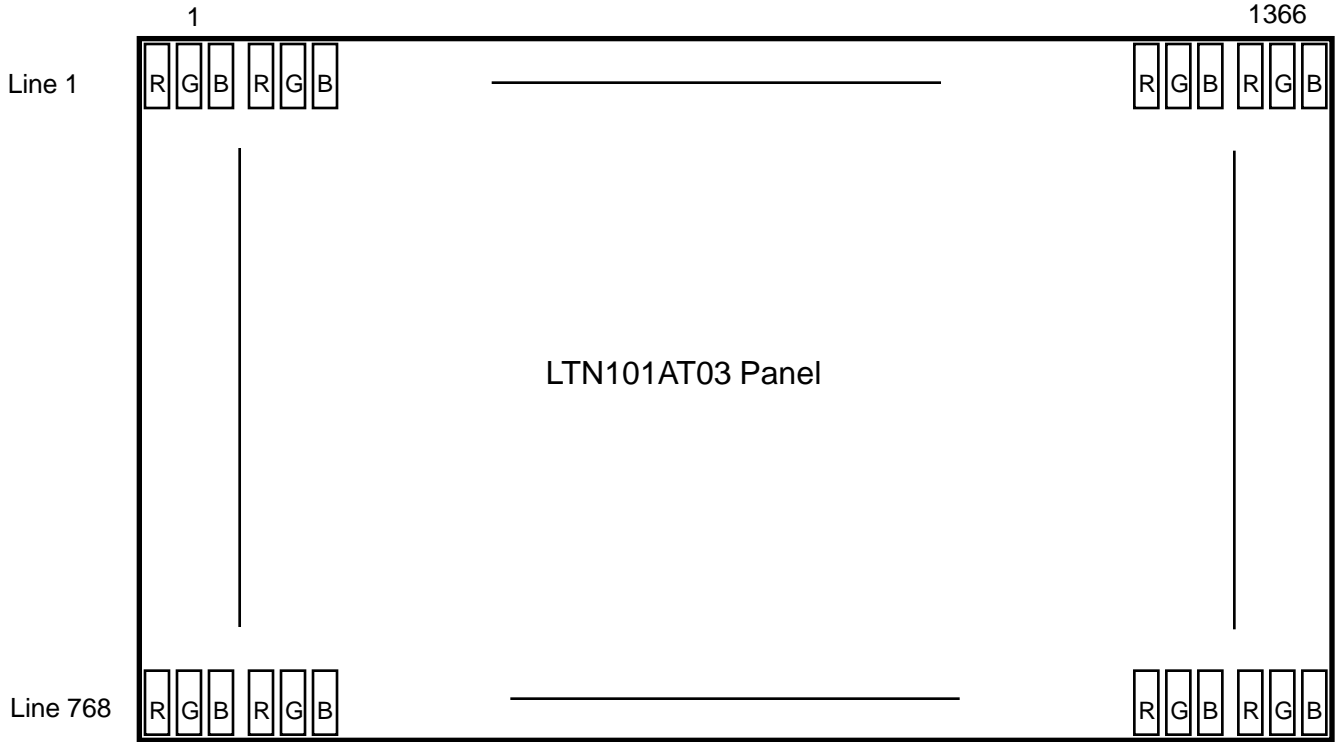
Note 1) Definition of gray :

Rn: Red gray, Gn: Green gray, Bn: Blue gray (n=gray level)

Note 2) Input signal: 0 =Low level voltage, 1=High level voltage

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5.5 Pixel Format in the display



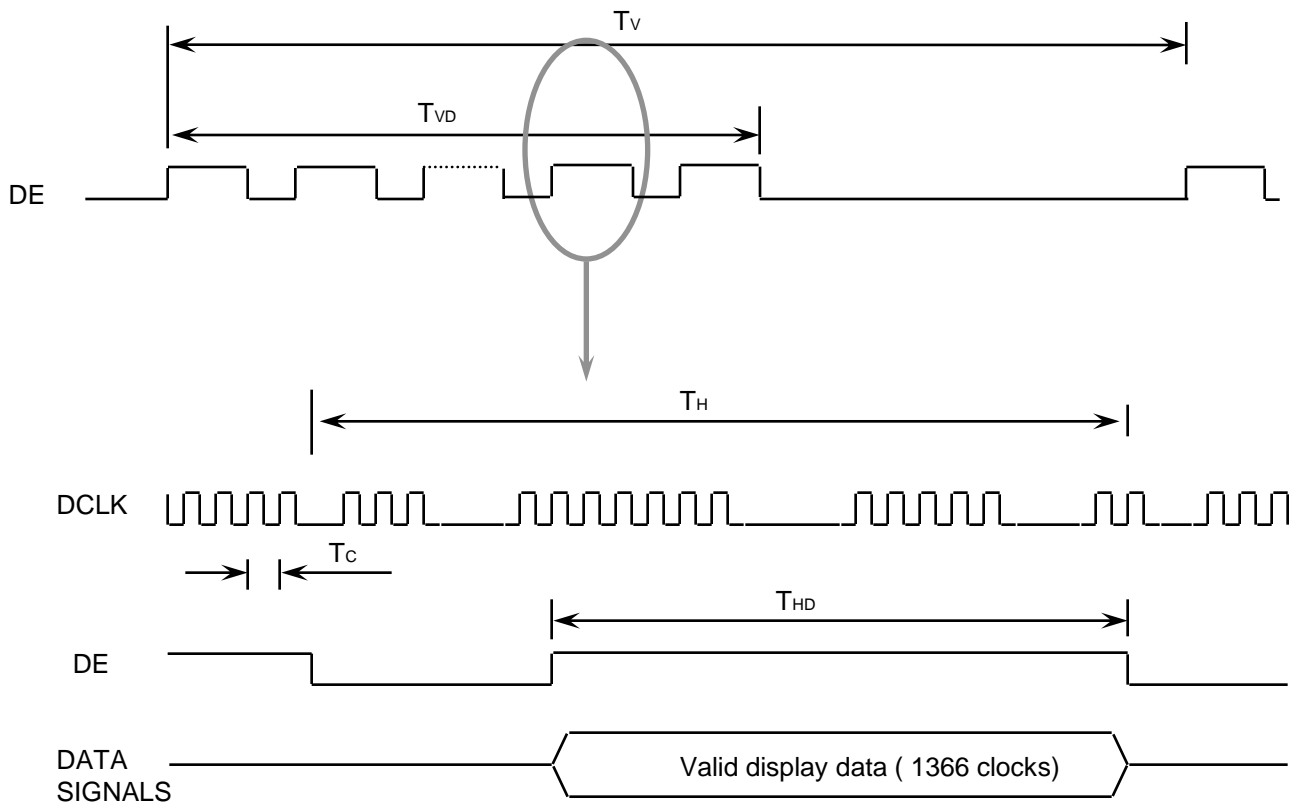
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6. INTERFACE TIMING

6.1 Timing Parameters

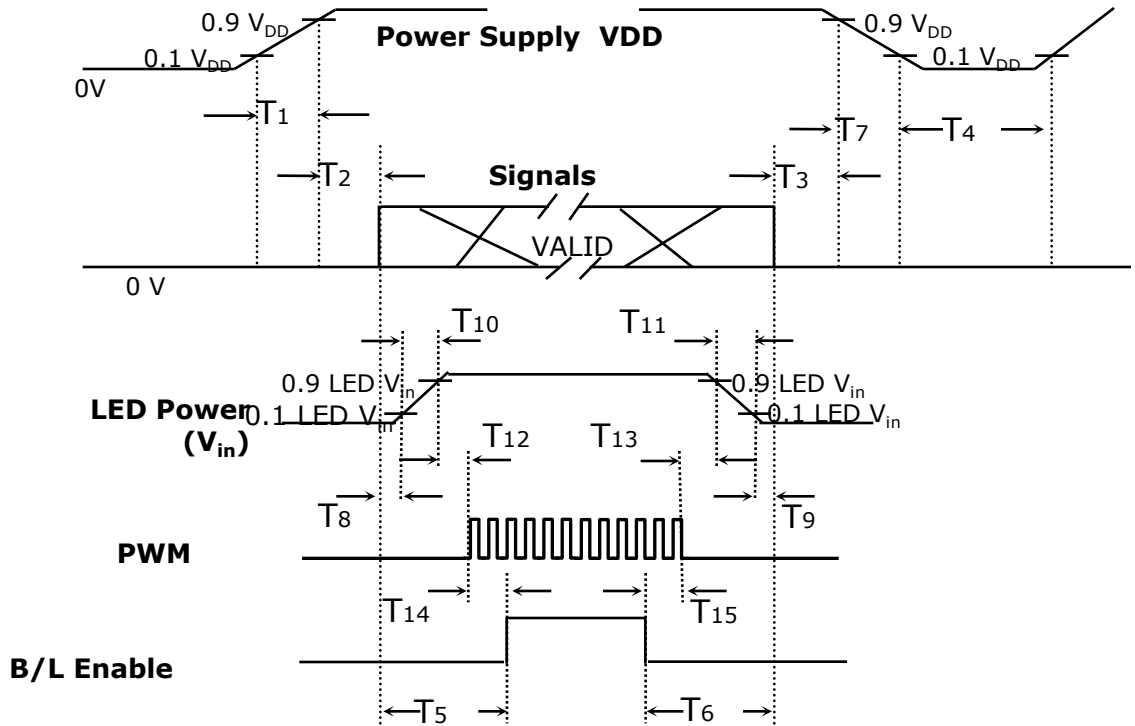
Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Frame Frequency	Cycle	TV	-	780	-	Lines	
Vertical Active Display Term	Display Period	TVD	-	768	-	Lines	
One Line Scanning Time	Cycle	TH	-	1489	-	Clocks	
Horizontal Active Display Term	Display Period	THD	-	1366	-	Clocks	

6.2 Timing diagrams of interface signal



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: To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



Power Sequence & Timing Parameters

Timing (ms)	Remarks
$0.5 < T_1 \leq 10$	V _{DD} rising time from 10% to 90%
$0 < T_2 \leq 50$	Delay from V _{DD} to valid data at power ON
$0 < T_3 \leq 50$	Delay from valid data OFF to V _{DD} OFF at power Off
$500 \leq T_4$	V _{DD} OFF time for Windows restart
$200 \leq T_5$	Delay from valid data to B/L enable at power ON
$200 \leq T_6$	Delay from valid data off to B/L disable at power Off
$0 < T_7 \leq 10$	V _{DD} falling time from 90% to 10%
$10 < T_8$	Delay from valid data on to LED driver V _{in} rising time 10%
$10 < T_9$	Delay from LED driver V _{in} falling time 10% to valid data Off
$0.5 < T_{10} \leq 10$	LED V _{in} rising time from 10% to 90%
$0.5 < T_{11} \leq 10$	LED V _{in} falling time from 90% to 10%
$10 < T_{12}$	Delay from LED driver V _{in} rising time 90% to PWM ON
$10 < T_{13}$	Delay from PWM Off to LED driver V _{in} falling time 10%
$10 < T_{14}$	Delay from PWM ON to B/L Enable ON
$10 < T_{15}$	Delay from B/L Enable Off to PWM Off

NOTE.

- (1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD} .
- (2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.
- (3) In case of $V_{DD} =$ off level, please keep the level of input signals on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

7. Mechanical Outline Dimension

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Refer to the next page

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Doc.No.

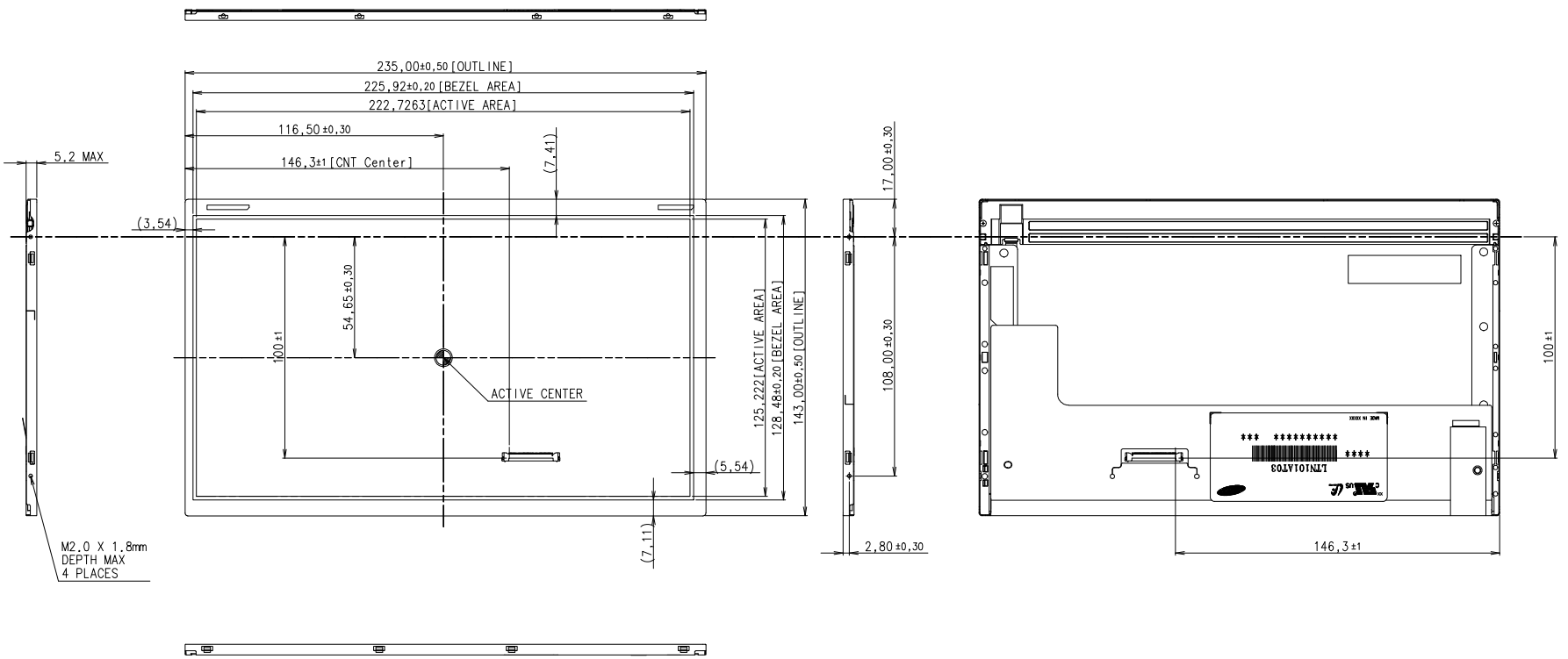
LTN101AT03-1

Rev.No

04-A00-G-110621

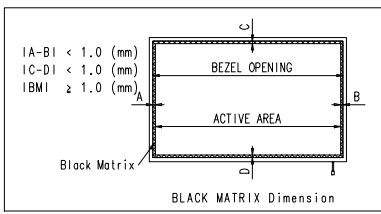
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- * NOTE
- SIGNAL INTERFACE CONNECTOR TO BE SPECIFIED AS BELOW.
 - MAKER : I-PEX
 - INPUT : I-PEX 20455-040E-0 or EQUIVALENT(LOCK TYPE)
 - ALLOWED DEPTH OF USERHOLE SCREW INSERTION IS 1.8mm MAX.
 - CALIFERS MEASURING FORCE : 750 gf
 - USER HOLE TORQUE SPEC : 2.5 kgfcm Max(5TIMES)
 - WEIGHT SPEC : 200g Max
 - IN ORDER TO AVOID IC DAMAGE, IT IS NOT ALLOW THAT OVERLAPPING OF CABLES OR ANTENNAS, CAMERA, WLAN, WWAN OVER THESE COF LACATIONS
 - GENERAL TOLERANCE IS 0.5mm

REV	DATE	DESCRIPTION OF REVISION	REASON	CHK'D BY
UNIT	mm	DRA'N BY DES'G BY CHK'D BY APP'D BY	MODEL NAME	LTN101AT03
SCALE	1:1	E.W.SEO	C.Y.BANG	
GENERAL TOLERANCE	±0.5	2010.05.10	2010.05.10	
SAMSUNG ELECTRONICS		PART/SHEET NAME	OUTLINE DIMENSION	SHEET 1/1
		SPEC. NO	CODE NO.	VER. 000

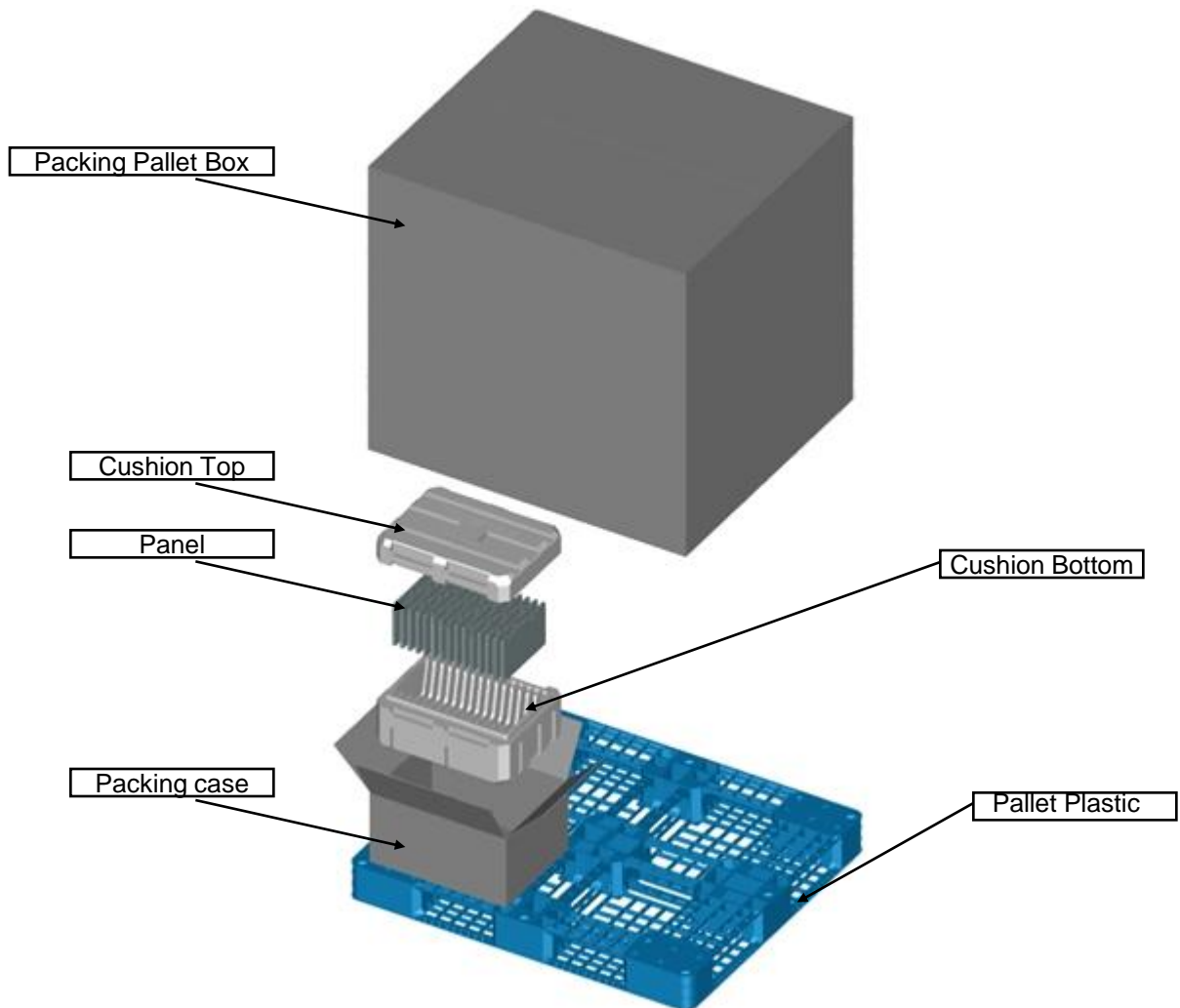
8. PACKING

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1. CARTON(Internal Package)

- (1) Packing Form
Styrofoam cushion box as shock absorber

(2) Packing Method



PACKING CASE

- Note 1) Total Weight : Approximately 7 kg
- 2) Acceptance number of piling : 30 sets
- 3) Carton size : 450(W) × 340(D) × 230(H)

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(3)Packing Material

No	Part name	Quantity
1	Static electric protective sack	30
2	Packing case (Inner box)	1 set
3	Pictorial marking	2 Pcs
4	Silicagel (500 x 1)	1
5	Carton	1 set

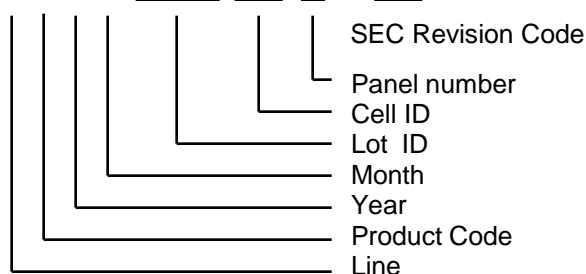
9. MARKINGS & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1)Parts number : LTN101AT03

(2)Revision code : 3 letters

(3)Lot number : X X X X XXX XX X **101**



(4) Nameplate Indication



Parts name : LTN1101AT03-1

Lot number : 6H8Jxxxxxx

Inspected work week : 1003




DP/N : Dell Part Number ("09FPCT" is for LTN101AT03-1)

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※ Panel revision code scheme

Build Name(s)	Revision Code(s)
SST (WS)	X00, X01, X02, ... X09
PT (ES)	X10, X11, X12, ... X19
ST (CS)	X20, X21, X23, ... X29
XB (MP)	A00, A01, A02, ... A99

(6) Packing box attach

SAMSUNG Elec. CHONAN CITY, CHUNGCHONGNAM-DO KOREA	
DP/N	09FPCT 
Box Qty 	C/O - KR 

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10. GENERAL PRECAUTIONS

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1. Handling

- (a) When the module is assembled, It should be attached to the system firmly using every mounting holes. Be careful not to twist and bend the modules.
- (b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module.
- (c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.
- (e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (f) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (h) Protect the module from static , it may cause damage to the C-MOS Gate Array IC.
- (i) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (j) Do not disassemble the module.
- (k) Do not pull or fold the lamp wire.
- (l) Do not adjust the variable resistor which is located on the back side.
- (m) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (n) Pins of I/F connector shall not be touched directly with bare hands.

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2. STORAGE

- (a) Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD module in direct sunlight.
- (c) The module shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

3. OPERATION

- (a) Do not connect,disconnect the module in the “ Power On” condition.
- (b) Power supply should always be turned on/off by following item 6.3 “ Power on/off sequence “.
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The standard limited warranty is only applicable when the module is used for general notebook applications. If used for purposes other than as specified, SEC is not to be held reliable for the defective operations. It is strongly recommended to contact SEC to find out fitness for a particular purpose.

4. OTHERS

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (the supply voltage variation, input voltage variation, variation in part contents and environmental temperature, so on) Otherwise the module may be damaged.
- (d) If the module displays the same pattern continuously for a long period of time,it can be the situation when the image “sticks” to the screen.
- (e) This module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.

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